

A detailed illustration depicting various aspects of program analysis. At the top center is a large red alarm bell with radiating lines. Below it, a man in a suit holds a megaphone. To the right, a woman looks through a large telescope. In the center, a large computer monitor displays a red 'X' icon and the word 'Error!'. A man in a white lab coat is kneeling in front of the monitor, gesturing towards it. To the left, a man in a suit and hard hat carries a large wrench. On the right, another man in a suit and hard hat holds a rolled-up document. The background features stylized grey shapes representing people or data. The title 'Program analysis' is written in large orange letters across the middle of the illustration.

Program analysis

Roberto Bruni, Roberta Gori
(University of Pisa)

Lecture #07

[\[source\]](#)

Taxonomy of program logics

**Different logics for different
purposes!!**

Hoare Logic

$$\{P\} \ c \ \{Q\}$$

$$\text{validity: } \llbracket c \rrbracket P \subseteq Q$$

$$\forall \sigma \in P . \forall \delta \in \llbracket c \rrbracket \sigma . \delta \in Q$$

can prove the absence of bugs
(any execution of c from P is correct)

Example

$\{x \leq 0, y = 1\}$

while($x \leq 5$) do $x := x + y$;

Example

$\{x \leq 0, y = 1\}$

while($x \leq 5$) do $x := x + y$;

$\{x = 6\}$

Example

$\{x \leq 0\}$

while($x \leq 5$) do $x := x + y$;

?? $\{x = 6\}$

Example

$\{x \leq 0\}$

while($x \leq 5$) do $x := x + y$;

$\{x \geq 6\}$

Example

$\{x \leq 0\}$

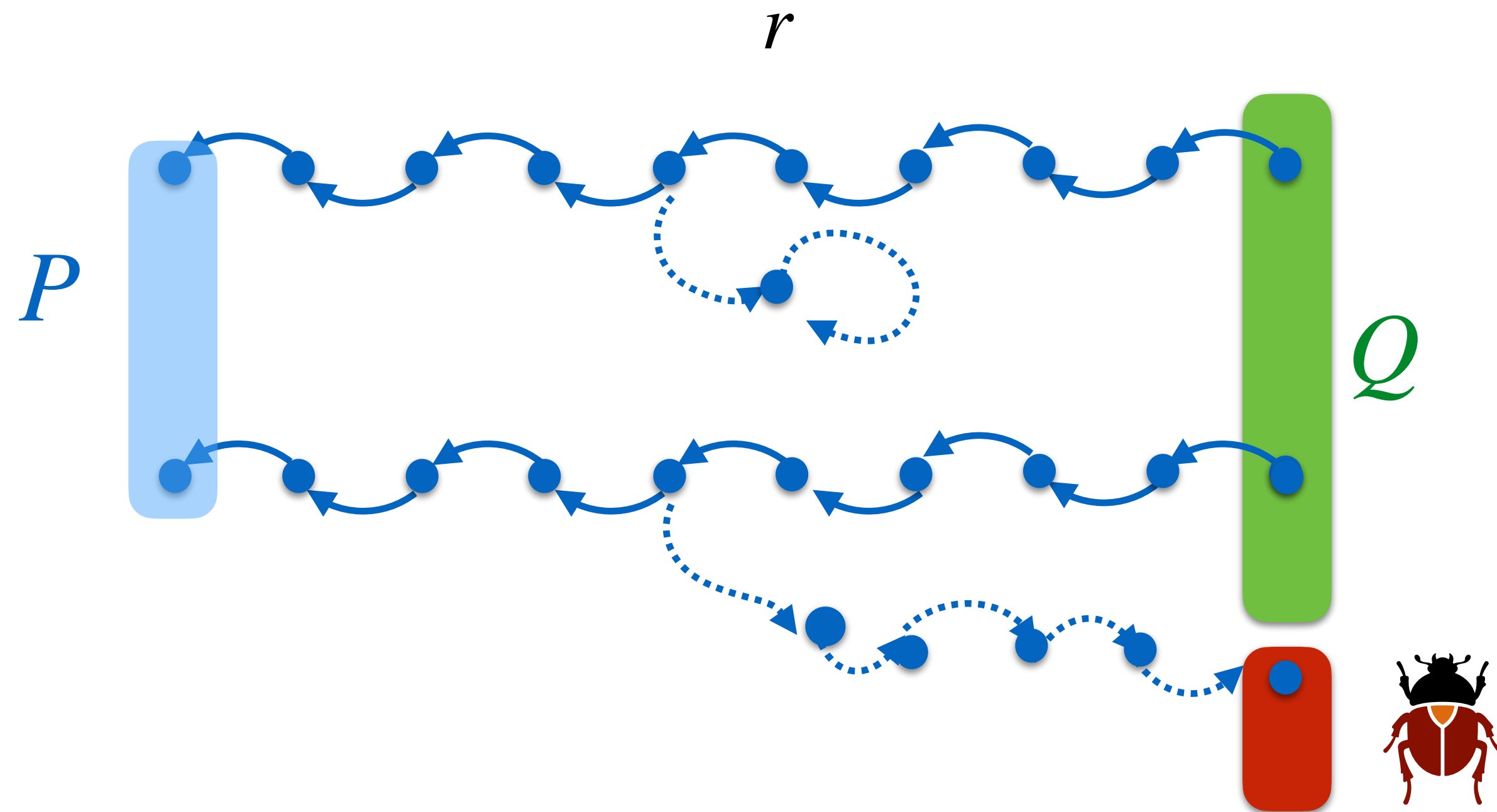
while($x \leq 5$) do $x := x + y$;

$\{x \geq 0\}$

Backward semantics

$$\llbracket \overleftarrow{r} \rrbracket \sigma' \triangleq \{ \sigma \mid \sigma' \in \llbracket r \rrbracket \sigma \}$$

$$\sigma \in \llbracket \overleftarrow{r} \rrbracket \sigma' \Leftrightarrow \sigma' \in \llbracket r \rrbracket \sigma$$



Necessary condition

$$(P) \text{ } c \text{ } (Q)$$

$$\text{validity: } P \supseteq \llbracket \overleftarrow{c} \rrbracket Q$$

$$\forall \delta \in Q. \forall \sigma \in \llbracket \overleftarrow{c} \rrbracket \delta. \sigma \in P$$

express necessary conditions for correctness
(any execution of c from outside P is incorrect)

Example

`while($x \leq 5$) do $x := x + y$;`

$(x = 6)$

Example

$$(x \leq 6 \wedge \exists n . n * y = 6 - x)$$

while($x \leq 5$) do $x := x + y$;

$$(x = 6)$$

Example

$(x \leq 6)$

`while($x \leq 5$) do $x := x + y$;`

$(x = 6)$

Incorrectness Logic (BUA)

$$[P] \ c \ [Q]$$

$$\text{validity: } \llbracket c \rrbracket P \supseteq Q$$

$$\forall \delta \in Q . \exists \sigma \in P . \delta \in \llbracket c \rrbracket \sigma$$

can prove the presence of bugs
(any error in Q is reachable executing c)

Example

$[x \leq 0]$

`while($x \leq 5$) do $x := x + y$;`

$??[x = 6]$

Reachable??

Example

$[x \leq 0]$

`while($x \leq 5$) do $x := x + y$;`

$??[x = 6]$

Reachable??

No!! $x = 6 \wedge y = -1$

Example

$[x \leq 0]$

`while($x \leq 5$) do $x := x + y$;`

$[x = 6 \wedge y > 0]$

Reachable??

Example

$[x \leq 0]$

`while($x \leq 5$) do $x := x + y$;`

$[x = 6 \wedge y > 0]$

Reachable??

$\forall y > 0, \exists \text{min } n . \quad 6 - (n * y) \leq 0, x = 6 - (n * y)$

Example

$[x \leq 8]$

`while($x \leq 5$) do $x := x + y$;`

$[x = 6 \wedge y > 0]$

Reachable??

Sufficient Incorrectness Logic (FUA)

$$\langle P \rangle \ c \ \langle Q \rangle$$

$$\text{validity: } P \subseteq \llbracket \overleftarrow{c} \rrbracket Q$$

$$\forall \sigma \in P . \exists \delta \in Q . \delta \in \llbracket c \rrbracket \sigma$$

express sufficient conditions for incorrectness
(any state in P can lead to *er* : Q)

Example

`while($x \leq 5$) do $x := x + y$;`

$\langle x = 6 \rangle$

Example

$$\langle x \leq 6 \wedge \exists n . n * y = 6 - x \rangle$$

while($x \leq 5$) do $x := x + y$;

$$\langle x = 6 \rangle$$

Example

$$\langle x \leq 6 \wedge y = 6 - x \rangle$$

while($x \leq 5$) do $x := x + y$;

$$\langle x = 6 \rangle$$

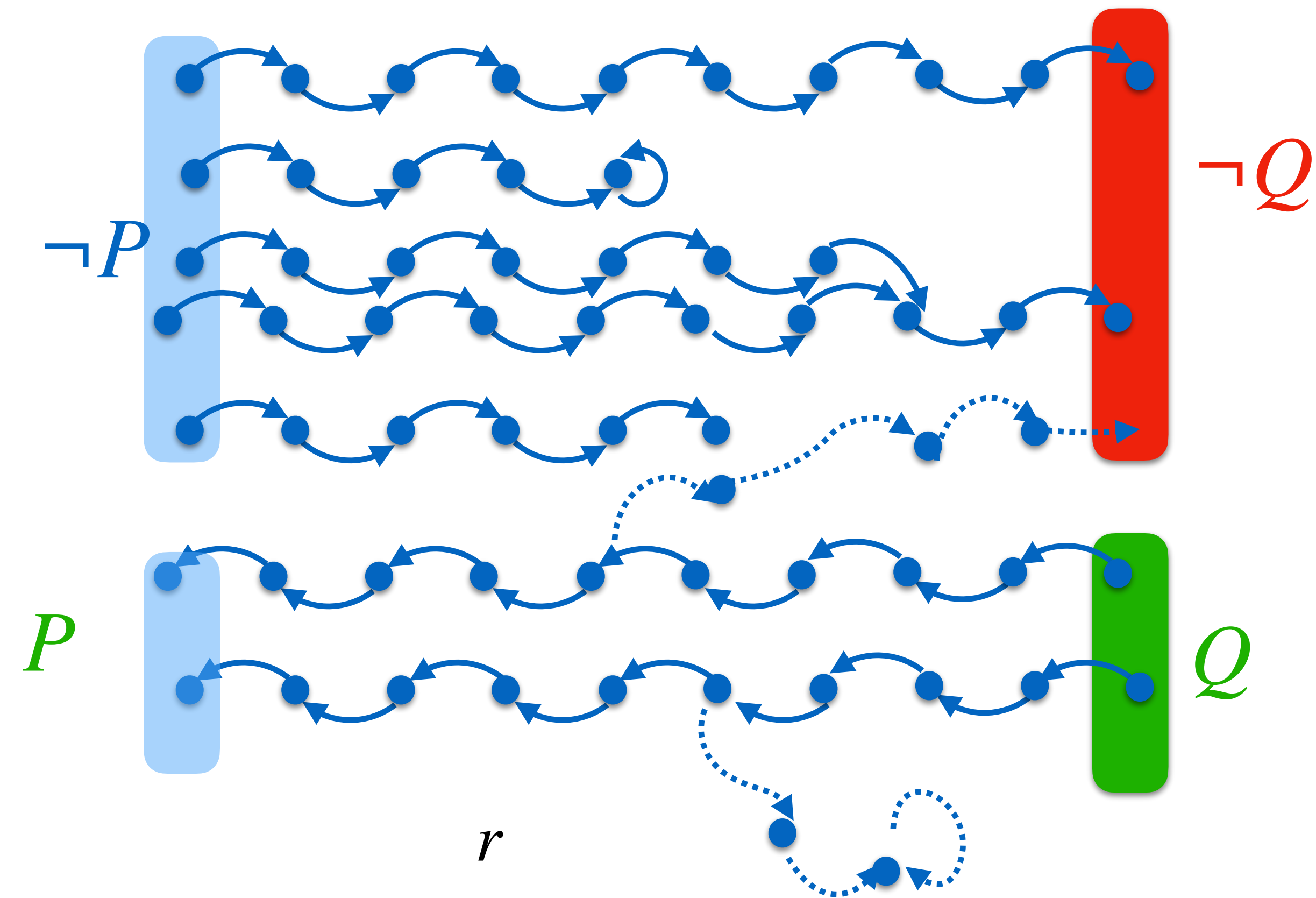
The taxonomy

	Forward	Backward
Over	HL $\{P\} \ c \ \{Q\}$ $\llbracket c \rrbracket P \subseteq Q$	NC $\langle P \rangle \ c \ \langle Q \rangle$ $P \supseteq \llbracket \overleftarrow{c} \rrbracket Q$
Under	IL $[P] \ c \ [Q]$ $\llbracket c \rrbracket P \supseteq Q$	SIL $\langle P \rangle \ c \ \langle Q \rangle$ $P \subseteq \llbracket \overleftarrow{c} \rrbracket Q$

Compare logics along the approximation axis

	Forward	Backward
Over	$\{\text{HL}\} \quad \llbracket r \rrbracket P \subseteq Q$	$(\text{NC}) \quad \llbracket \overleftarrow{r} \rrbracket Q \subseteq P$
Under	$\llbracket \text{IL} \rrbracket \quad \llbracket r \rrbracket P \supseteq Q$	$\langle\langle \text{SIL} \rangle\rangle \quad \llbracket \overleftarrow{r} \rrbracket Q \supseteq P$

NC vs HL



$$\{\neg P\} \ r \ \{\neg Q\} \iff (P) \ r \ (Q)$$

$$\llbracket r \rrbracket \neg P \subseteq \neg Q \iff \llbracket \overleftarrow{r} \rrbracket Q \subseteq P$$

Compare logics along the approximation axis

	Forward	Backward
Over	$\{\text{HL}\} \quad \llbracket r \rrbracket P \subseteq Q$	$(\text{NC}) \quad \llbracket \overleftarrow{r} \rrbracket Q \subseteq P$
Under	$\llbracket \text{IL} \rrbracket \quad \llbracket r \rrbracket P \supseteq Q$	$\langle\langle \text{SIL} \rangle\rangle \quad \llbracket \overleftarrow{r} \rrbracket Q \supseteq P$

Compare logics along the approximation axis

	Forward	Backward
Over	$\{\text{HL}\} \quad \llbracket r \rrbracket P \subseteq Q$	$(\text{NC}) \quad \llbracket \overleftarrow{r} \rrbracket Q \subseteq P$
Under	$\llbracket \text{IL} \rrbracket \quad \llbracket r \rrbracket P \supseteq Q$	$\langle\langle \text{SIL} \rangle\rangle \quad \llbracket \overleftarrow{r} \rrbracket Q \supseteq P$

SIL vs IL

c_{42} :

```
if even (x) {  
    if odd(y) { z := 42; }  
}
```

Safe $z \neq 42$
E.g., $x := 1 / (42 - z)$

No relations!

Given a specification of the possible errors

$$Q \triangleq \{ z = 42 \}$$

With **IL** one can prove

$$[z=11] \ c_{42} \ [z=42 \wedge \textit{odd}(y) \wedge \textit{even}(x)]$$

Expressing that the postcondition is reachable

With **SIL** one can prove

$$\langle\langle z=11 \wedge \textit{odd}(y) \wedge \textit{even}(x) \rangle\rangle \ c_{42} \ \langle\langle z=42 \rangle\rangle$$

Expressing a precondition that leads to error states

Compare logics according to the consequence rule

	Forward	Backward
Over	$\{\text{HL}\} \quad \llbracket r \rrbracket P \subseteq Q$	$(\text{NC}) \quad \llbracket \overleftarrow{r} \rrbracket Q \subseteq P$
Under	$[\text{IL}] \quad \llbracket r \rrbracket P \supseteq Q$	$\langle\langle \text{SIL} \rangle\rangle \quad \llbracket \overleftarrow{r} \rrbracket Q \supseteq P$

$$P \Rightarrow P' \quad \frac{P' \text{ } r \text{ } Q' \quad Q' \Rightarrow Q}{P \text{ } r \text{ } Q}$$

Consequence rules follows the diagonal of the schema, so they suggest relations between HL-SIL and IL-NC

Compare logics according to the consequence rule

	Forward	Backward	
Over	{HL} $\llbracket r \rrbracket P \subseteq Q$	(NC) $\llbracket \overleftarrow{r} \rrbracket Q \subseteq P$	$\frac{P' \Rightarrow P \quad \textcolor{blue}{P'} r \textcolor{blue}{Q'} \quad Q \Rightarrow Q'}{\textcolor{blue}{P} r \textcolor{blue}{Q}}$
Under	[IL] $\llbracket r \rrbracket P \supseteq Q$	«SIL» $\llbracket \overleftarrow{r} \rrbracket Q \supseteq P$	$\frac{P \Rightarrow P' \quad \textcolor{blue}{P'} r \textcolor{blue}{Q'} \quad Q' \Rightarrow Q}{\textcolor{blue}{P} r \textcolor{blue}{Q}}$

Consequence rules follows the diagonal of the schema, so they suggest relations between HL-SIL and IL-NC

Relations following the diagonals

NC-IL: no relation

HL-SIL: loosely related,

r **deterministic** and **terminating**: **SIL** equivalent to **HL**

$$\langle\langle P \rangle\rangle \ r \ \langle\langle Q \rangle\rangle \Leftrightarrow \{P\} \ r \ \{Q\}$$

SIL vs HL

c_{42} :

$x := \text{nondet}();$

if *even* (x) {

 if *odd*(y) { $z := 42;$ }

}

Safe $z \neq 42$
E.g., $x := 1/(42 - z)$

Given a specification of the possible errors

$Q \triangleq \{ z = 42 \}$

With **SIL** one can prove

$\langle\langle \text{odd}(y) \rangle\rangle c_{42} \langle\langle z=42 \rangle\rangle$

Expressing a precondition that leads to error states

With **HL** one can prove

$\{ z=42 \} c_{42} \{ z=42 \}$

Sufficient incorrectness logic (SIL)

OOPSLA 2025

Revealing Sources of (Memory) Errors via Backward Analysis

FLAVIO ASCARI, University of Pisa, Italy
ROBERTO BRUNI, University of Pisa, Italy
ROBERTA GORI, University of Pisa, Italy
FRANCESCO LOGOZZO, Meta Platforms, USA

Sound over-approximation methods are effective for proving the absence of errors, but inevitably produce false alarms that can hamper programmers. In contrast, under-approximation methods focus on bug detection and are free from false alarms. In this work, we present two novel proof systems designed to locate the source of errors via backward under-approximation, namely Sufficient Incorrectness Logic (SIL) and its specialization for handling memory errors, called Separation SIL. The SIL proof system is minimal, sound and complete for Lisbon triples, enabling a detailed comparison of triple-based program logics across various dimensions, including negation, approximation, execution order, and analysis objectives. More importantly, SIL lays the foundation for our main technical contribution, by distilling the inference rules of Separation SIL, a sound and (relatively) complete proof system for automated backward reasoning in programs involving pointers and dynamic memory allocation. The completeness result for Separation SIL relies on a careful crafting of both the assertion language and the rules for atomic commands.

CCS Concepts: • **Theory of computation** → **Logic and verification**; *Proof theory*; *Hoare logic*; **Separation logic**; *Programming logic*.

Additional Key Words and Phrases: Sufficient Incorrectness Logic, Incorrectness Logic, Outcome Logic

ACM Reference Format:

Flavio Ascari, Roberto Bruni, Roberta Gori, and Francesco Logozzo. 2025. Revealing Sources of (Memory) Errors via Backward Analysis. *Proc. ACM Program. Lang.* 9, OOPSLA1, Article 127 (April 2025), 28 pages. <https://doi.org/10.1145/3720486>

1 Introduction

Formal methods aim to automate the improvement of software reliability and security. Notable success stories are, e.g., the Astrée static analyzer [Blanchet et al. 2003], the SLAM model checker [Ball and Rajamani 2001], the certified C compiler CompCert [Leroy 2009], VCC for safety properties verification [Cohen et al. 2009], and the Frama-C platform for the integration of many C code analyses [Baudin et al. 2021]. Despite that, effective program correctness methods struggle to reach mainstream adoption, mostly because they exploit over-approximation to handle decidability issues and false positives are seen as a distraction by expert programmers. Being free from false positives is possibly the reason why *under-approximation* approaches for bug-finding, such as testing and bounded model checking, are preferred in industrial applications. Incorrectness Logic (IL) [O’Hearn 2020] is a new program logic for bug-finding: *any error state found in the post can be produced by some input states that satisfy the pre*. However, IL triples are not able to characterize precisely *the input states that are responsible for a given error*. This is possibly rooted in the *forward* flavor of the under-approximation, which follows the ordinary direction of code execution.

Authors’ Contact Information: Flavio Ascari, University of Pisa, Pisa, Italy, flavio.ascari@phd.unipi.it; Roberto Bruni, University of Pisa, Pisa, Italy, bruni@di.unipi.it; Roberta Gori, University of Pisa, Pisa, Italy, roberta.gori@unipi.it; Francesco Logozzo, Meta Platforms, Seattle, USA, logozzo@meta.com.



This work is licensed under a Creative Commons Attribution-NonCommercial-ShareAlike 4.0 International License.

© 2025 Copyright held by the owner/author(s).

ACM 2475-1421/2025/4-ART127

<https://doi.org/10.1145/3720486>

“SIL can characterise the source of errors”



Sufficient Incorrectness Logic (SIL)

Given Q a specification of the possible errors

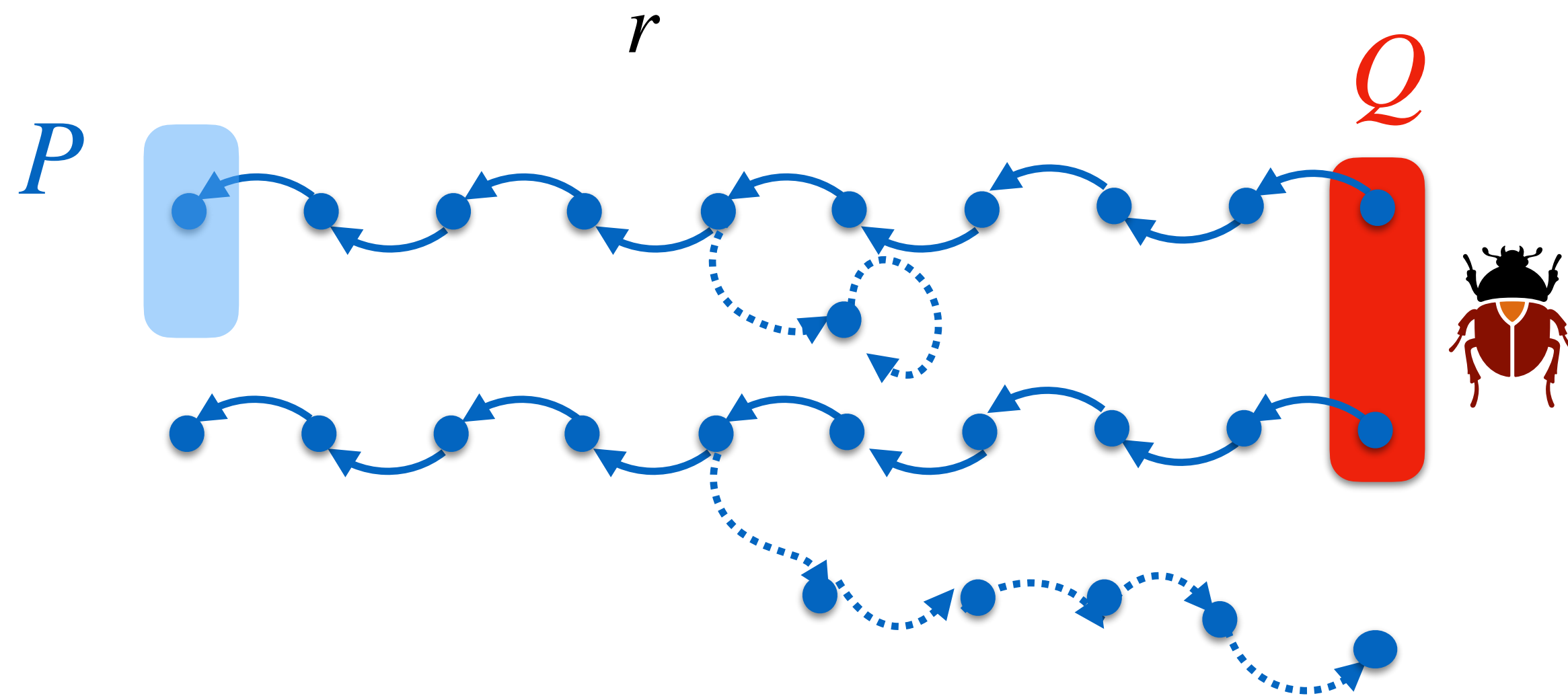
$\langle\langle P \rangle\rangle c \langle\langle Q \rangle\rangle$ is valid when

It is an under-approximation!

$$[[\overleftarrow{r}]]Q \supseteq P$$

means

$$\forall \sigma \in P \exists \sigma' \in Q . \sigma' \in [[r]]\sigma$$



An **under-approximating** logic designed to devise the initial states leading to errors

Manifest errors

An error is **manifest** if it occurs **independently of the context** and is therefore particularly interesting to point out to programmers

Manifest errors cannot be characterised with IL

But they can be easily characterised with SIL

$\langle\langle \textit{true} \rangle\rangle r \langle\langle Q \rangle\rangle$ is valid $\Leftrightarrow Q$ is a **manifest error**

SIL Rules

The proof system favours backward analysis starting from the (error) postconditions

Hoare's axiom for assignment

$$\frac{}{\langle\langle Q[a/x] \rangle\rangle x := a \langle\langle Q \rangle\rangle} \langle\langle atom - a \rangle\rangle$$

$$\langle\langle y > 0 \rangle\rangle \quad x := y - 1 \quad \langle\langle x \geq 0 \rangle\rangle$$

$$\langle\langle y \neq 43 \rangle\rangle \quad x := y - 1 \quad \langle\langle x \neq 42 \rangle\rangle$$

SIL Rules

The proof system favours backward analysis starting from the (error) postconditions

$$\frac{}{\langle\langle Q \cap b \rangle\rangle b? \langle\langle Q \rangle\rangle} \langle\langle atom - g \rangle\rangle$$

$$\begin{array}{ll} \langle\langle \emptyset \rangle\rangle (x > 0)? & \langle\langle x = -42 \rangle\rangle \\ \langle\langle x = 42 \rangle\rangle (x > 0)? & \langle\langle x = 42 \rangle\rangle \end{array}$$

SIL Rules

The proof system favours backward analysis starting from the (error) postconditions

Same conditions for both branches

$$\frac{\langle\langle P_1 \rangle\rangle r_1 \langle\langle Q \rangle\rangle \quad \langle\langle P_2 \rangle\rangle r_2 \langle\langle Q \rangle\rangle}{\langle\langle P_1 \cup P_2 \rangle\rangle r_1 + r_2 \langle\langle Q \rangle\rangle} \langle\langle choice \rangle\rangle$$

$$\begin{array}{lll} \langle\langle y = 43 \vee y = 42 \rangle\rangle & (x := y - 1) + (x := y) & \langle\langle x = 42 \rangle\rangle \\ \langle\langle true \rangle\rangle = \langle\langle y \neq 43 \vee y \neq 42 \rangle\rangle & (x := y - 1) + (x := y) & \langle\langle x \neq 42 \rangle\rangle \\ \langle\langle y \neq 43 \rangle\rangle & (x := y - 1) + (x := 42) & \langle\langle x \neq 42 \rangle\rangle \end{array}$$

SIL Rules

The proof system favours backward analysis starting from the (error) postconditions

Backward iteration starting from final state Q_0

$$\frac{\forall n \geq 0. \langle\langle Q_{n+1} \rangle\rangle \ r \ \langle\langle Q_n \rangle\rangle}{\langle\langle \bigcup_{n \geq 0} Q_n \rangle\rangle \ r^* \ \langle\langle Q_0 \rangle\rangle} \quad \langle\langle iter \rangle\rangle$$

$$\langle\langle x \leq 42 \rangle\rangle = \langle\langle \dots \vee x = 41 \vee x = 42 \rangle\rangle \ (x := x + 1)^* \quad \langle\langle x = 42 \rangle\rangle$$

SIL Rules

The proof system favours backward analysis starting from the (error) postconditions

SIL can drop disjunction going backward:

$$\frac{}{\langle\!\langle \emptyset \rangle\!\rangle \ r \ \langle\!\langle Q \rangle\!\rangle} \quad \langle\!\langle empty \rangle\!\rangle \qquad \frac{\langle\!\langle P \cup P' \rangle\!\rangle \ r \ \langle\!\langle Q \rangle\!\rangle}{\langle\!\langle P \rangle\!\rangle \ r \ \langle\!\langle Q \rangle\!\rangle} \quad \langle\!\langle cons' \rangle\!\rangle$$

$$\langle\!\langle x = 41 \vee x = 42 \rangle\!\rangle \ (x := x + 1)^* \quad \langle\!\langle x = 42 \rangle\!\rangle$$

Validity, soundness and completeness

A proof system for SIL

Core rules

$$\begin{array}{c}
 \frac{}{\llbracket [\overleftarrow{c}] Q \rrbracket c \llbracket Q \rrbracket} \text{atom} \\
 \frac{\llbracket P_1 \rrbracket r_1 \llbracket Q \rrbracket \quad \llbracket P_2 \rrbracket r_2 \llbracket Q \rrbracket}{\llbracket P_1 \cup P_2 \rrbracket r_1 + r_2 \llbracket Q \rrbracket} \text{choice} \\
 \frac{\forall n \geq 0. \llbracket Q_{n+1} \rrbracket r \llbracket Q_n \rrbracket}{\llbracket \bigcup_{n \geq 0} Q_n \rrbracket r^* \llbracket Q_0 \rrbracket} \text{iter} \\
 \frac{P \subseteq P' \quad \llbracket P' \rrbracket r \llbracket Q' \rrbracket \quad Q' \subseteq Q}{\llbracket P \rrbracket r \llbracket Q \rrbracket} \text{cons} \\
 \frac{\llbracket P \rrbracket r_1 \llbracket R \rrbracket \quad \llbracket R \rrbracket r_2 \llbracket Q \rrbracket}{\llbracket P \rrbracket r_1; r_2 \llbracket Q \rrbracket} \text{seq}
 \end{array}$$

Additional rules

$$\begin{array}{c}
 \frac{}{\llbracket \emptyset \rrbracket r \llbracket Q \rrbracket} \text{empty} \\
 \frac{\llbracket P_1 \rrbracket r \llbracket Q_1 \rrbracket \quad \llbracket P_2 \rrbracket r \llbracket Q_2 \rrbracket}{\llbracket P_1 \cup P_2 \rrbracket r \llbracket Q_1 \cup Q_2 \rrbracket} \text{disj} \\
 \frac{}{\llbracket Q \rrbracket r^* \llbracket Q \rrbracket} \text{iter0} \\
 \frac{\llbracket P \rrbracket r^*; r \llbracket Q_1 \rrbracket}{\llbracket P \cup Q_2 \rrbracket r^* \llbracket Q_1 \cup Q_2 \rrbracket} \text{unroll-split} \\
 \frac{\llbracket P \rrbracket r^*; r \llbracket Q \rrbracket}{\llbracket P \rrbracket r^* \llbracket Q \rrbracket} \text{unroll}
 \end{array}$$

Soundness and completeness

SIL validity of a triple : $\llbracket \overleftarrow{r} \rrbracket Q \supseteq P$

Th. [*Soundness*]

All provable triples (including additional rules) are valid

Th. [*Completeness*]

All valid triples are provable (using the core rules)

Questions

Question 1

Which SIL triples are valid for any r and P ?

$\langle\langle \text{false} \rangle\rangle \text{ } r \text{ } \langle\langle P \rangle\rangle$



$\langle\langle \text{true} \rangle\rangle \text{ } r \text{ } \langle\langle \text{true} \rangle\rangle$



$\langle\langle P \rangle\rangle \text{ } r^* \text{ } \langle\langle P \vee x = 0 \rangle\rangle$



$\langle\langle wlp(r, P) \rangle\rangle \text{ } r \text{ } \langle\langle P \rangle\rangle$



Question 2

Prove that rule [conj] is **unsound** for SIL

$$\frac{\langle\!\langle P_1 \rangle\!\rangle \text{ } r \text{ } \langle\!\langle Q_1 \rangle\!\rangle \quad \langle\!\langle P_2 \rangle\!\rangle \text{ } r \text{ } \langle\!\langle Q_2 \rangle\!\rangle}{\langle\!\langle P_1 \wedge P_2 \rangle\!\rangle \text{ } r \text{ } \langle\!\langle Q_1 \wedge Q_2 \rangle\!\rangle} \text{ [conj]}$$

Consider $\langle\!\langle x = 0 \rangle\!\rangle \text{ } x := \text{nondet}() \text{ } \langle\!\langle x = 0 \rangle\!\rangle$

and $\langle\!\langle x = 0 \rangle\!\rangle \text{ } x := \text{nondet}() \text{ } \langle\!\langle x = 1 \rangle\!\rangle$

By rule [conj] we could derive $\langle\!\langle x = 0 \rangle\!\rangle \text{ } x := 1 \text{ } \langle\!\langle \text{false} \rangle\!\rangle$

which is not sound!

Question 3

Prove or disprove the validity of the following axiom in SIL

$$\langle\!\langle P \rangle\!\rangle (b)? \quad \langle\!\langle P \wedge b \rangle\!\rangle$$

Consider the following triple $\langle\!\langle x \geq 0 \rangle\!\rangle (x > 1)? \quad \langle\!\langle x \geq 2 \rangle\!\rangle$

which is not a valid triple since from $x=0$ we cannot reach $x \geq 2$

Exercise

// function r

```
x := nondet();
if (x=1) {
  if (y≤100) {
    C }}
```

// function C-McCarthy 91 function

```
while (x>0) {
  if (y>100) {
    y := y-10; x := x-1 }
  else
    y := y+11; x := x+1 } }
```

	SIL	IL	HL	NC
[true] r $[y = 91 \wedge x \neq 1]$	X	✓	X	✓
$\langle\langle y \leq 100 \rangle\rangle$ r $\langle\langle y = 91 \wedge x \neq 1 \rangle\rangle$	✓	✓	X	✓
$\langle\langle y \leq 100 \rangle\rangle$ r $\langle\langle y = 91 \rangle\rangle$	✓	X	X	✓
$\langle\langle y < 91 \rangle\rangle$ r $\langle\langle y = 91 \rangle\rangle$	✓	X	X	X